IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

◈IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®

Welcome
United States Patent and Trademark Office

IEEE Xplore®
1 Million Documents
1 Million Users

» Search Results

Help   FAQ   Terms   IEEE Peer Review   **Quick Links**   ▼

Your search matched **23** of **1079782** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

(cnn<in>ti) <and> (cmos <in>ti)   Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

---

1 **A low-power CMOS implementation of programmable CNN's with embedded photosensors**
Anguita, M.; Pelayo, F.J.; Fernandez, F.J.; Prieto, A.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on] , Volume: 44 , Issue: 2 , Feb. 1997
Pages:149 - 153

[Abstract]   [PDF Full-Text (176 KB)]   **IEEE JNL**

---

2 **Analog CMOS implementation of a discrete time CNN with programmable cloning templates**
Anguita, M.; Pelayo, F.J.; Prieto, A.; Ortega, J.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volume: 40 , Issue: 3 , March 1993
Pages:215 - 218

[Abstract]   [PDF Full-Text (332 KB)]   **IEEE JNL**

---

3 **A CNN-driven locally adaptive CMOS image sensor**
Carmona, R.; Dominguez-Matas, C.M.; Cuadri, J.; Jimenez-Garrido, F.; Rodriguez-Vazquez, A.;
Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on , Volume: 5 , 23-26 May 2004
Pages:457 - 460

[Abstract]   [PDF Full-Text (234 KB)]   **IEEE CNF**

---

4 **An analog CMOS chip implementing a CNN-based locomotion controller for quadruped walking robots**
Nakada, K.; Asai, T.; Amemiya, Y.;
Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on , Volume: 3 , 23-26 May 2004

Home
What Can I Access?
Log-out

Journals & Magazines
Conference Proceedings
Standards

By Author
Basic
Advanced
CrossRef

Join IEEE
Establish IEEE Web Account
Access the IEEE Member Digital Library

Access the IEEE Enterprise File Cabinet

Print Format

Pages:III - 1-4 Vol.3

[Abstract]   [PDF Full-Text (391 KB)]   **IEEE CNF**

5 A universal CNN neuron for CMOS technology: model and functional capabilities
Dogaru, R.; Chitu, C.; Glesner, M.;
Signals, Circuits and Systems, 2003. SCS 2003. International Symposium on , Volume: 1 , 10-11 July 2003
Pages:181 - 184 vol.1

[Abstract]   [PDF Full-Text (361 KB)]   **IEEE CNF**

6 Implementation of non-linear templates using a decomposition technique by a 0.5 µm CMOS CNN universal chip
Linan, G.; Foldesy, P.; Rodrignez-Vazquez, A.; Espejo, S.; Dominguez-Castro, R.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 2 , 28-31 May 2000
Pages:401 - 404 vol.2

[Abstract]   [PDF Full-Text (368 KB)]   **IEEE CNF**

7 Review of CMOS implementations of the CNN universal machine-type visual microprocessors
Roska, T.; Rodriguez-Vazquez, A.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 2 , 28-31 May 2000
Pages:120 - 123 vol.2

[Abstract]   [PDF Full-Text (304 KB)]   **IEEE CNF**

8 Modeling nanoelectronic CNN cells: CMOS, SETs and QCAs
Gerousis, C.; Goodnick, S.M.; Xiaohui Wang; Porod, W.; Csurgay, A.I.; Toth, G.; Lent, C.S.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 1 , 28-31 May 2000
Pages:274 vol.1

[Abstract]   [PDF Full-Text (76 KB)]   **IEEE CNF**

9 Robust CMOS CNN implementation with respect to manufacturing inaccuracies
Paasio, A.; Dawidziuk, A.; Halonen, K.; Porra, V.;
Cellular Neural Networks and their Applications, 1996. CNNA-96. Proceedings., 1996 Fourth IEEE International Workshop on , 24-26 June 1996
Pages:381 - 385

[Abstract]   [PDF Full-Text (264 KB)]   **IEEE CNF**

10 Real-life application case studies using CMOS 0.8 µm CNN universal chip: analogic algorithm for motion detection and texture segmentation
Foldesy, P.; Zarandy, A.; Szolgay, P.; Sziranyi, T.;
Cellular Neural Networks and their Applications, 1996. CNNA-96. Proceedings., 1996 Fourth IEEE International Workshop on , 24-26 June 1996
Pages:363 - 368

[Abstract]   [PDF Full-Text (300 KB)]   **IEEE CNF**

11 Pulse stream current mode CMOS CNN chip

Paasio, A.; Dawidziuk, A.; Porra, V.;
Cellular Neural Networks and their Applications, 1996. CNNA-96. Proceedings.,
1996 Fourth IEEE International Workshop on , 24-26 June 1996
Pages:457 - 460

[Abstract]   [PDF Full-Text (176 KB)]   **IEEE CNF**

12 CMOS implementation of an extended CNN cell to deal with complex dynamics

Giustolisi, G.; Rizzo, A.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
Symposium on , Volume: 5 , 25-28 May 2003
Pages:V-761 - V-764 vol.5

[Abstract]   [PDF Full-Text (307 KB)]   **IEEE CNF**

13 Accurate CMOS implementation of PWL CNN neuron activations

Forti, M.; Pancioni, L.; Rocchi, S.; Vignoli, V.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium
on , Volume: 1 , 26-29 May 2002
Pages:I-221 - I-224 vol.1

[Abstract]   [PDF Full-Text (372 KB)]   **IEEE CNF**

14 CMOS realization of a 2-layer CNN universal machine chip

Carmona, R.; Jimenez-Garrido, F.; Dominguez-Castro, R.; Espejo, S.; Rodriguez-Vazquez, A.;
Cellular Neural Networks and Their Applications, 2002. (CNNA 2002). Proceedings
of the 2002 7th IEEE International Workshop on , 22-24 July 2002
Pages:444 - 51,xviii-x

[Abstract]   [PDF Full-Text (357 KB)]   **IEEE CNF**

15 An efficient and compact integration of CMOS image sensors and cellular neural network (CNN) for intelligent processing

Chung Yu Wu; Wen Cheng Yen;
Multisensor Fusion and Integration for Intelligent Systems, 1999. MFI '99.
Proceedings. 1999 IEEE/SICE/RSJ International Conference on , 15-18 Aug. 1999
Pages:232 - 236

[Abstract]   [PDF Full-Text (224 KB)]   **IEEE CNF**

1 2 Next

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC
Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

◈IEEE

Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore

Welcome
United States Patent and Trademark Office

IEEE *Xplore*
1 Million Documents
1 Million Users

Help   FAQ   Terms   IEEE Peer Review

Quick Links   ▼

» Search Results

Welcome to IEEE Xplore

○- Home
○- What Can
   I Access?
○- Log-out

Table of Contents

○- Journals
   & Magazines
○- Conference
   Proceedings
○- Standards

Search

○- By Author
○- Basic
○- Advanced
○- CrossRef

Member Services

○- Join IEEE
○- Establish IEEE
   Web Account
○- Access the
   IEEE Member
   Digital Library

○- Access the
   IEEE Enterprise
   File Cabinet

🖶 Print Format

Your search matched **23** of **1079782** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

(cnn<in>ti) <and> (cmos <in>ti) | Search |

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

---

16 **The design of CMOS cellular neural network (CNN) using the neuron-bipolar junction transistor (vBJT)**
Chiou-Ling Yeh; Chung-Yu Wu;
Neural Networks, 1999. IJCNN '99. International Joint Conference on , Volume: 4 , 10-16 July 1999
Pages:2337 - 2342 vol.4

[Abstract]   [PDF Full-Text (432 KB)]   **IEEE CNF**

---

17 **A novel compact architecture for a programmable full-range CNN in 0.5 µm CMOS technology**
Hegt, J.A.; Leenaerts, D.M.W.; Wilmans, R.T.;
Cellular Neural Networks and Their Applications Proceedings, 1998 Fifth IEEE International Workshop on , 14-17 April 1998
Pages:288 - 293

[Abstract]   [PDF Full-Text (348 KB)]   **IEEE CNF**

---

18 **A 0.5 µm CMOS CNN analog random access memory chip for massive image processing**
Carmona, R.; Espejo, S.; Dominguez-Castro, R.; Rodriguez-Vazque, A.; Roska, T.; Kozek, T.; Chua, L.O.;
Cellular Neural Networks and Their Applications Proceedings, 1998 Fifth IEEE International Workshop on , 14-17 April 1998
Pages:271 - 276

[Abstract]   [PDF Full-Text (724 KB)]   **IEEE CNF**

---

19 **Challenges in mixed-signal IC design of CNN chips in submicron CMOS**
Rodriguez-Vazquez, A.; Dominguez-Castro, R.; Espejo, S.;
Cellular Neural Networks and Their Applications Proceedings, 1998 Fifth IEEE International Workshop on , 14-17 April 1998
Pages:13

[Abstract]   [PDF Full-Text (100 KB)]   **IEEE CNF**

---

20 **Electro-optical measurement system for the DC characterization of visible detectors for CMOS compatible CNN vision chips**
Roca, E.; Frutos, F.; Espejo, S.; Dominguez-Castro, R.; Rodriguez-Vazquez, A.;
Cellular Neural Networks and Their Applications Proceedings, 1998 Fifth IEEE International Workshop on , 14-17 April 1998
Pages:282 - 287

[Abstract]   [PDF Full-Text (648 KB)]   **IEEE CNF**

---

21 **Realization of a CNN universal chip in CMOS technology**
Espejo, S.; Dominguez-Castro, R.; Rodriguez-Vazquez, A.;
Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on , Volume: 1 , 28 April-3 May 1995
Pages:657 - 659 vol.1

[Abstract]   [PDF Full-Text (312 KB)]   **IEEE CNF**

---

22 **A CNN universal chip in CMOS technology**
Dominguez-Castro, R.; Espejo, S.; Rodriguez-Vazquez, A.; Carmona, R.;
Cellular Neural Networks and their Applications, 1994. CNNA-94., Proceedings of the Third IEEE International Workshop on , 18-21 Dec. 1994
Pages:91 - 96

[Abstract]   [PDF Full-Text (328 KB)]   **IEEE CNF**

---

23 **Accurate design of analog CNN in CMOS digital technologies**
Rodriquez-Vazques, A.; Dominguez-Castro, R.; Huertas, J.L.;
Cellular Neural Networks and their Applications, 1990. CNNA-90 Proceedings., 1990 IEEE International Workshop on , 16-19 Dec. 1990
Pages:273 - 280

[Abstract]   [PDF Full-Text (356 KB)]   **IEEE CNF**